



## UNITED STATES PATENT AND TRADEMARK OFFICE



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/476,622	12/31/1999	Howard Chin	884.101US1	8079	
7	590 07/16/2002				
Schwegman Lundberg Woessner & Kluth PA P O Box 2938 Minneapolis, MN 55402			EXAMINER		
			TREAT, WILLIAM M		
			ART UNIT	PAPER NUMBER	
			2183		

DATE MAILED: 07/16/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

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•	Application No.	Applicant(s)	\ 1	· <del>-</del> ·
Office Action Summary	09/476,6	122 Chi	netali	
y	Examiner /	REAT	Group Art Unit 2183	
—The MAILING DATE of this communicat	tion appears on the cover s	heet beneath the co	orrespondence addre	ess
Period for Reply				
A SHORTENED STATUTORY PERIOD FOR REPLY OF THIS COMMUNICATION.	Y IS SET TO EXPIRE 3 (+)	hrce) MONTH(S	) FROM THE MAILING	3 DATE
<ul> <li>Extensions of time may be available under the provisions from the mailing date of this communication.</li> <li>If the period for reply specified above is less than thirty (3 - If NO period for reply is specified above, such period sha</li> <li>Failure to reply within the set or extended period for reply</li> </ul>	30) days, a reply within the statutorall, by default, expire SIX (6) MONT	ry minimum of thirty (30) FHS from the mailing dat	days will be considered tine of this communication.	
Status				
🗷 Responsive to communication(s) filed on 🔣	1/4/02	<del>.</del>		•
☐ This action is FIMAL.				
☐ Since this application is in condition for allowar accordance with the practice under Ex parte C			the merits is closed	in
Disp sition of Claims		,		
Claim(s) 10 and 21-40	is/are ;	is/are pending in the application.		
Of the above claim(s) 21 - 45		is/are withdrawn from consideration.		
☐ Claim(s)		is/are a	allowed.	
ß Claim(s) \ 0	·	is/are r	rejected.	
☐ Claim(s)	The state of the s	is/are o	objected to.	
□ Claim(s)————————————————————————————————————		are sul		lection
Application Papers		1		
☐ See the attached Notice of Draftsperson's Pate	=			
☐ The proposed drawing correction, filed on		• •	d.	
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Pri rity under 35 U.S.C. § 119 (a)-(d)				
<ul> <li>□ Acknowledgment is made of a claim for foreign</li> <li>□ All □ Some* □ None of the CERTIFIED</li> <li>□ received.</li> </ul>		` ' ' '		
<ul> <li>□ received in Application No. (Series Code/Se</li> <li>□ received in this national stage application from the companion of t</li></ul>	· · · · · · · · · · · · · · · · · · ·		·	
*Certified copies not received:			·	
Attachment(s)				
• •	Q. Papar No(a)	☐ Interview Sumn	nary, PTO-413	
☐ Information Disclosure Statement(s), PTO-144	9, rapel 140(5)			
• •	9, Faper No(5)		nal Patent Application,	PTO-152

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- 1. Claims 10 and 21-40 are presented for examination.
- 2. Newly submitted claims 21-40 are directed to an invention that is independent or distinct from the invention originally claimed for the following reasons:

Applicants specification identifies the invention of their new claims 21-40 as a distinct invention from the original claim 10 and their canceled claims.

On page 4, line 25 through page 5, line 3, applicants state: "Fig. 2A is a more detailed block diagram of an example embodiment of the processor and firmware shown in Fig. 1. In one embodiment of the invention, firmware 206 stores program code 210 for controlling the operation of the processor 204. The programmed code 210 stored in the firmware 206 is referred to herein as the 'firmware code.' In an example embodiment, the firmware code 210 implements microcode operations using registers which are specific to a particular machine or to a particular model of a machine. The registers are referred to herein as 'Machine Specific Registers.' The machine specific registers function as an interface between the firmware 206 and the processor 204." Applicants new claims (21-40) are all directed to this new invention requiring machine specific registers.

On page 5, line 25 through page 6, line 4, applicants state: "Fig. 2B is a block diagram of an alternate embodiment of the processor shown in Fig. 1 and external microcode stored in a computer readable medium. In one embodiment of the invention, a computer readable medium 220, which is external to the processor, stores program 222 for controlling the operation of the processor 224. Examples of computer readable mediums external to the processor include, but

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are not limited to, mass storage devices, firmware, and memory. The programmed code 222 stored in the computer readable medium is referred to herein as 'external microcode.' In an example embodiment, the external microcode 222 implements microcode operations by controlling hardware logic on the processor 224 without the use of the registers (i.e., the machine specific registers) shown in Fig. 2A." Applicants' original claim 10 and their canceled claims were all directed to the alternative invention without machine specific registers.

Because these inventions are distinct for the reasons given above and the search required for claims 21-40 is not required for claim 10, restriction for examination purposes as indicated is proper.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claims 21-40 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

- 3. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(I).
- 4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 10 is rejected under 35 U.S.C. 102(b) as being clearly anticipated by Demers et al.

(WO 94/12929).

6. The examiner would recommend applicants read page 8, line 25 through page 9, line 3

before responding. The examiner would also note it is inherent in Demers that the microcode of

Demers controls one or more functions of the processor by directly triggering hardware on the

processor.

7. The examiner regrets his typographical error which caused him to earlier omit the formal

rejection of claim 10 from the body of his previous action while still noting claim 10's rejection on

the Office Action Summary. This time the examiner has made sure the explanation for the

rejection of claim 10 is clear in the body of his action.

8. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to William M. Treat whose telephone number is (703) 305-9699. The

examiner works a flexible schedule, but he can normally be reached during the afternoons and

evenings on four of the five weekdays.

WILLIAM M. TREAT PRIMARY EXAMINER

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